

Content Inspection – QDR Interface Product Brief

Features

- Silicon based, regular expression processor.
- Deterministic wire-rate performance, 1Gbps, 2.5Gbps, 5Gbps or 10Gbps
- Up to 128 DFA engines.
- Supports pattern matching across multiple buffers.
- Look-aside architecture with a QDR interface compatible with Intel IXP 28xx NPUs.
- Integrates a low latency memory controller for RLDRAMII or DDR2-3 devices.
- Optional:
 - TCP-IP header processing and checking.
 - URL Normalization and URL Filtering
 - TCAM Interface and TCAM Lookups
- Optimized implementations:
 - Xilinx Virtex 5
 - Altera Stratix 2
 - Altera Stratix 3

